

1. Personal information

Name: Mihai TIMIS
Academic degree: Assistant Professor, B.Eng. (Hons), PhD
University: Automatic Control and Computer Engineering Faculty, Technical University “Gh.Asachi”, Iasi, Romania
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2. Education and training

- 2001 – 2007 (PhD) in Computer Science & Engineering. PhD thesis title: Contributions to the Analysis and Synthesis of Asynchronous Digital Systems and Implementation into FPGA Devices – Automatic Control and Computer Science and Engineering Faculty, “Gh.Asachi” Technical University of Iasi, Romania, November 2007
- 2000 –2001 (MSc) Master degree in Computer Science&Engineering – Automatic Control and Computer Science Faculty, “Gh. Asachi“ Technical University of Iasi, Romania , June 2001
- 1995 – 2000 (DSc) Diplomat Engineer degree in Computer Science&Engineering - Automatic Control and Computer Science Faculty, “Gh. Asachi“ Technical University of Iasi, Romania, June 2000

Degrees:

- **PhD** in Computer Science & Engineering - *october 2007*
- **Msc** (Master degree in Computer Science&Engineering) *june 2001*
- **Dsc** (Diplomat Engineer degree in Computer Science&Engineering) *june 2000*

3. Work experience

2007 – present, assistant professor, B.Eng. (Hons), PhD

2001 – 2007 university assistant

2000 – 2001 university preparator

4. Scientific research interest

- Digital Design
- Digital Verification
- VLSI Design of Digital Systems with ASIC, FPGA
- C/C++ Programming Language
- Digital Systems and Embedded Microcontroller Architecture
- 2 book chapters printed on Research Updates in Mathematics and Computer Science
- 8 technical books as first author
- Over 100 scientific papers wrote on prestigious national/international computer engineering symposiums
- Official paper reviewer at World E-Conference Journal Series on Science, Technology and Development
- Reviewer at the Transactions on Signal Processing Journal